



501.40678CX1

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: J. NOGUCHI, et al

Application No.: 10/742,932

Filed: DECEMBER 23, 2003

For: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Group AU: 1765

Examiner: Lan Vinh

Confirm. No: 7382

AMENDMENT

Mail Stop: AMEND - FEE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

February 3, 2006

Sir:

In response to the Office Action mailed August 3, 2005, the period for response having been extended for three (3) months by the attached Petition for Extension of Time, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.